QUESTION BANK

COA(PT-1)

1. Draw and explain memory hierarchy (Speed, capacity, cost)
2. Explain RAM/ROM.
3. Explain cache memory based on (definition, SRAM, speed, size, levels, architecture(LA,LT), write policies, mapping )
4. Compare different cache mapping functions.
5. Numerical on page replacement policies-FIFO, LRU, Optimal.
6. Compare best fit/Worst fit/ next fit/ First fit.
7. Numerical on the above allocation algorithms.
8. Explain Von Neumann model.
9. Differentiate between computer organization and computer architecture.
10. Explain pre-fetch instruction queue of 8086 microprocessor.
11. Explain 8086 architecture with neat diagram microprocessor..
12. Explain general purpose registers of 8086 microprocessor..
13. Explain Flag register of 8086 microprocessor.
14. Explain memory segment and memory segment register of 8086 microprocessor.